

LB1934T

# **3V Camera 1.5-Channel Driver**

## Overview

The LB1934T is a low-voltage, low-saturation forward/ reverse driver IC with built-in 1.5-channel bridge. Its compact, low-profile package makes it ideal for use in compact cameras powered by 3V lithium batteries.

Using direct microcontroller drive, it is possible to control standby, forward rotation, reverse rotation, and braking of two motors for film rewind and lens drive by means of only three control signals. The 0.5-channel section of the 1.5-channel bridge can be divided into sink/source output, allowing configurations such as 1-channel bridge + sink output + source output.

## Features

- Low voltage drive possible.
- When using same power supply for  $V_{CC}$  and  $V_S$ :  $V_{CC}=V_S=1.4V$  min.

When using separate power supply for  $V_{CC}$  and  $V_S$ :  $V_S$ =1.0V min.,  $V_{CC}$ =1.4V min.

- Low saturation voltage : residual voltage (upper side transistor + lower side transistor) =0.5V typ. at Io=1A.
- Zero power dissipation in standby mode.
- Direct microcontroller drive possible (any strength relationships of voltage supported between microcontroller control signal and  $V_{CC}$  or  $V_S$ ).
- Logic power supply and motor power supply can be supplied at separate pins.
- Built-in thermal protection circuit.
- Number of control signals can be optimized for the set. "2-motor control" or "1-motor + 2-load control" can be implemented by 3 control signals.

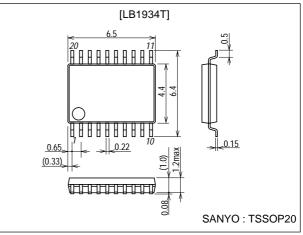
In addition to the above, 4 control signals allow implemention of a "input ignore mode" where two control signals are used in conjunction with other IC input signals.

- Built-in spark killer diode.
- Compact, low-profile package (TSSOP-20; thickness=1.1 mm typ.)

## Package Dimensions

### unit:mm

#### 3246-TSSOP20



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## **Specifications**

## Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
	V <sub>S</sub> max	VS	-0.3 to +8.0	V
Maximum supply voltage	V <sub>CC</sub> max	V <sub>CC</sub>	-0.3 to +8.0	V
Maximum output current	IO max1	OUT1, OUT2	2000	mA
	I <sub>O</sub> max2	OUT3P, OUT3N	1500	mA
Maximum applied output voltage	V <sub>O</sub> max	OUT1, OUT2, OUT3P, OUT3N	-0.3 to VS+VSF	V
Maximum applied input voltage	V <sub>I</sub> max	MD1, MD2, IN1, IN2	-0.3 to +8.0	V
Allowable power dissipation	Pd max	With specified substrate*	850	mW
Operating temperature	Topr		-20 to +70	°C
Storage temperature	Tstg		-55 to +150	°C

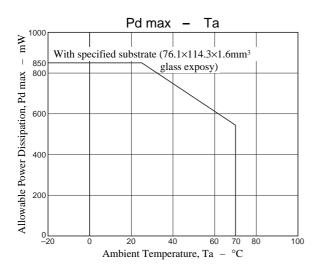
\*Specified substrate : 76.1×114.3×1.6mm<sup>3</sup> (glass exposy)

#### Allowable Operating Ranges at $Ta = 25^{\circ}C$

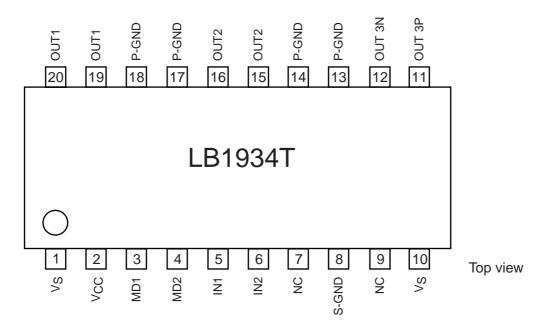
Parameter	Symbol	Conditions	Ratings	Unit
Power aupply veltage	٧ <sub>S</sub>	٧ <sub>S</sub>	1.0 to 7.0	V
Power supply voltage	VCC	V <sub>CC</sub>	1.4 to 7.0	V
Input high-level voltage	VINH	MD1, MD2, IN1, IN2	1.4 to 7.0	V
Input low-level voltage	VINL	MD1, MD2, IN1, IN2	-0.3 to +0.4	V

## **Electrical Characteristics** at Ta = 25°C, $V_S$ = $V_{CC}$ =3V

Parameter	Cumbal	Conditions		Ratings			
Parameter	Symbol	Conditions	min	typ	max	Unit	
[Operating current drain]	ł		•				
Standby mode current drain	ISTB	ICC+IS		0.1	3	μΑ	
VS operating current drain 1	I <sub>S</sub> 1	(Forward/reverse)		70	90	mA	
VS operating current drain 2	I <sub>S</sub> 2	(Brake)		75	95	mA	
VCC operating current drain 1	I <sub>CC</sub> 1	(1ch/2ch/half-bridge mode)		4	5	mA	
VCC operating current drain 1	I <sub>CC</sub> 1	(IN1/IN2 input ignore mode)		1	2	mA	
[Output circuits]	ł		•		•		
OUT1/OUT2 output saturation voltage	V <sub>O</sub> sat1	(PNP+NPN) Io=1000mA		0.5	0.75	V	
OUT3P output saturation voltage	V <sub>O</sub> sat2	(PNP) I <sub>O</sub> =1000mA		0.4	0.55	V	
OUT3N output saturation voltage	V <sub>O</sub> sat3	(NPN) I <sub>O</sub> =1000mA		0.3	0.45	V	
Spark killer diode reverse current	I <sub>S</sub> (leak)				10	μΑ	
Spark killer diode forward voltage VSF		OUT1, OUT2, OUT3P, OUT3N			1.7	V	
[Input circuit]							
Input high-level current	IINH	V <sub>IN</sub> =5V (MD1, MD2, IN1, IN2)		70	100	μA	
Input low-level current	I <sub>IN</sub> L	V <sub>IN</sub> =0V (MD1, MD2, IN1, IN2)			0	μA	



### **Pin Assignment**



#### **Pin Function**

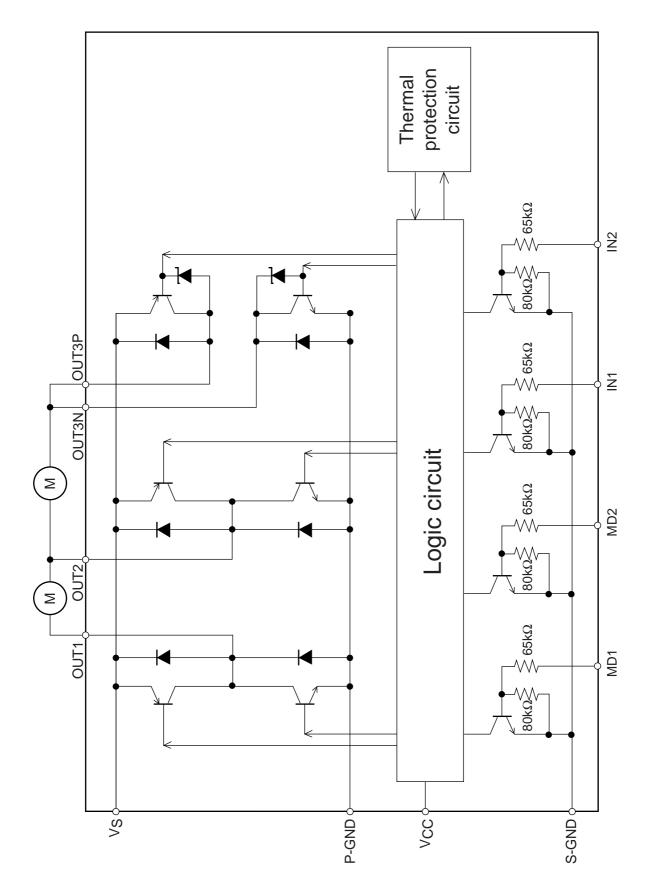
Pin number	Pin name	Function
1, 10	VS	Motor drive power supply pin (+)
2	VCC	Control system power supply pin (+)
13, 14, 17, 18	P-GND	Motor drive ground pin (-)
8	S-GND	Control system ground pin (-)
19, 20	OUT1	Motor drive output pin (sink/source)
15, 16	OUT2	Motor drive output pin (sink/source)
11	OUT3P	Motor drive output pin (source)
12	OUT3N	Motor drive output pin (sink)
3	MD1	Control input pin (High active)
4	MD2	Control input pin (High active)
5	IN1	Control input pin (High active)
6	IN2	Control input pin (High active)
7, 9	NC	Not used

#### **Truth Table**

	Input			Ou	tput		Mode			
MD1	MD2	IN1	IN2	OUT1	OUT2	OUT3P	OUT3N	Mode		
		L	L					1 ch	Standby	
	L	Н	L	н	L				Forward rotation	
		L	н	L	н				Reverse rotation	
		Н	н	н	н				Brake	
		L	L						Standby	
	н	Н	L		L	н		2 ab	Forward rotation	
	п	L	н		н		L	2 ch	Reverse rotation	
		Н	н		н	н			Brake	
		L	L						Standby	
н		Н	L			н			Source	
	H L	L	н				L	Half-bridge	Sink	
		Н	н			н			Source	
Н	Н	*	*					Disregard IN1/IN2 input		

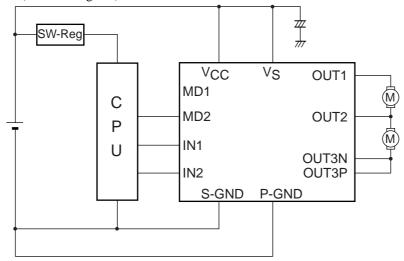
Note : "\*" in the input column means "don't care". Blank in the output column means "off".

## Block Diagram

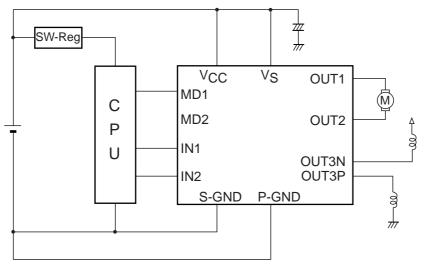


### **Sample Application Circuits**

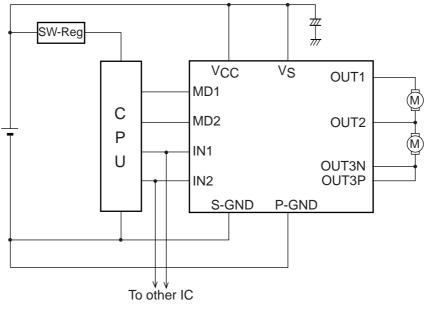
1) 2-motor control (3 control signals)

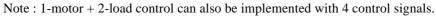


#### 2) 1-motor + 2-load control (3 control signals)



3) 2-motor control (4 control signals)





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